ABOUT ANNA UNIVERSITY

Anna University was established on 4th September 1978. It offers higher education in Engineering, Technology and Allied Sciences relevant to the current and projected needs of the society. Besides promoting research and disseminating knowledge gained there from, it fosters cooperation between the academic and industrial communities.

ABOUT MIT CAMPUS

Madras Institute of Technology is one of the premier technical institutions started in the year 1949 by Shri. C. Rajam, an eminent industrialist. The institute is established as a result of a bold experiment in technical education as it introduced for the first time, totally unconventional Engineering courses such as Aeronautical Engineering, Automobile Engineering, Electronics Engineering and Instrumentation Engineering. It was merged with Anna University in the year 1978.

ABOUT THE DEPARTMENT

The Department of Electronics Engineering established in the year 1949, has its core strength in the leading areas of Electronics & Communication Technology. The academic programmes offered in the Department are B.E (Electronics and Communication Engineering), M.E (Communication and Networking, VLSI Design and Embedded Systems & Wireless Technologies). The cutting-edge research areas include Artificial Intelligence, Image Processing & Pattern Recognition, Communication Technologies, Network Security, Sensor Networks, Optical Communication, Signal Processing, Embedded Systems and VLSI. The Department has collaborative partners from academia and industry both within India and worldwide.

LOCATION

Madras Institute of Technology campus is located in Chrompet, Chennai, Tamil Nadu, India and the campus is adjacent to Chrompet railway station.

ABOUT THE FDP

The main objective of this FDP is to explore the possibilities and challenges of utilizing Next Generation Algorithms and Technological Advances in the field of Advanced Computing. The fields include Augmented and Virtual Reality, Vision Transformer and its Applications, Multicore Architectures and systems, Architectural advancement in Anomaly Detection, Evolution of GPT Models, Industry 4.0 and Beyond, Predictive analytics, The Art of Possibility: Blending Human Augmentation and Generative AI for Creative Master, Testing for ADAS & Automotive Embedded firmware development and Deep Insights of VAE & GAN Architecture with Deep Fake Hands-on. This course offers a platform to provide faculty members and scholars with the opportunity to gain knowledge on Next Generation algorithms and Technological Advances. This course is intended to facilitate collaboration among faculty members and to explore interdisciplinary research opportunities and encourage them towards the development of joint research projects, grant proposals, and publications.

OUTCOME OF THE FDP

The outcome of the Faculty Development Program focused on utilizing Next-Generation Algorithms and Technological Advances in the field of Advanced Computing is likely to be multifaceted:

- Enhanced Understanding of Next-Generation Algorithms and its Applications.
- Acquired practical knowledge is crucial for integrating Algorithms into their teaching and research activities.
- Knowledge dissemination to students.
- Participants in the FDP would have the opportunity to network with experts and peers in the field of Next-Generation Algorithms and Technological Advances.
- Fostering innovation, collaboration and skill development within the academic community.

AICTE Training and Learning (ATAL) Academy

Sponsored 6-Days Online Faculty Development Programme on

Next-Generation Algorithms and Technological Advances

(Online Mode)

06.01.2025 to 11.01.2025

Organized by

DEPARTMENT OF ELECTRONICS ENGINEERING
MIT Campus, Anna University, Chennai-44

In Collaboration with

CENTRE FOR FACULTY & PROFESSIONAL DEVELOPMENT
Anna University, Chennai-25

Coordinators

Dr. K.MARIAMMAL
Associate Professor, Electronics Engineering, MIT Campus, Anna University

Dr. V.SATHIESH KUMAR
Assistant Professor (Sr.Gr.), Electronics Engineering, MIT Campus, Anna University

WHO CAN APPLY?

The Faculty members, Research scholars & PG Scholars of the AICTE approved institutions and Industry Personnel are eligible to apply.

HOW TO APPLY?

Use the following link to apply for the FDP through ATAL portal: https://atalacademy.aicte-india.org/login

SELECTION

Candidates satisfying the eligibility criteria will be selected on First-come-first-served basis. Selected candidates will be intimated by e-mail only. Confirmation of participation is to be made by email within the mentioned date positively. The participants should submit the authorization certificate signed from the principal on day-1 of the FDP.

SUCCESSFUL COMPLETION

The certificates shall be issued to those participants who are registered on ATAL portal www.aicte-india.org/atal and attend the program with minimum 80% attendance and score minimum 70% marks in the test.

IMPORTANT DATES

Submission of Application: 25.12.2024
Intimation of Selection: 26.12.2024
Confirmation by Participants: 27.12.2024

RESOURCE PERSONS

Sessions will be administered by subject Experts from Industries of high repute, R&D organizations and Academia (Overseas & India).

ORGANIZING COMMITTEE

CHIEF PATRON:

CONVENER COMMITTEE MEMBERS
Anna University

PATRON:

Prof. J.PRAKASH
Registrar
Prof. K. RAVICHANDRAN
Dean, MIT Campus

CHAIR:

Prof. P.VANAJA RANJAN
Director, CFPD
Prof. V.ADAIKKALAM
Addl. Director, CFPD

CO-CHAIR:

Prof. Dr. D. MEGANATHAN
HoD, Electronics, MIT Campus

COORDINATOR:

Dr. K. MARIAMMAL
Electronics, MIT Campus

CO-COORDINATOR:

Dr. V. SATHIESH KUMAR
Electronics, MIT Campus

Venue: Online Meeting in Teams Platform

ADDRESS FOR COMMUNICATION
The Coordinator, Online ATAL FDP (NGATA)
Department of Electronics Engineering, MIT Campus, Anna University, Chrompet, Chennai-600044,
E-mail: elexmitworkshop@gmail.com
Phone: 7338861638, Mobile: 7358064510

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DECLARATION

I declare that all the details furnished in my application are true to the best of my knowledge and I agree to abide by the rules and regulations governing the conduct of FDP under ATAL Academy.

Date:
Place: Signature of the Participant

AUTHORIZATION CERTIFICATE

This is to certify that_____, working as _____ in the Department of _____ is a regular employee of our institution and is hereby permitted to attend the Online Six days ATAL FDP on "Next-generation Algorithms and Technological Advances" from 06.01.2025 to 11.01.2025, Organized by Department of Electronics Engineering, MIT Campus, Anna University, Chromepet, Chennai – 600 044.

Date: Signature of the competent
Place: Authority with seal

ATAL Online 6 Day Faculty Development Programmes 2024 -25 Schedule

FDP Thrust Area: Advanced Computing (Supercomputing, AI and Quantum Computing)
FDP Title: Next-Generation Algorithms and Technological Advances
Start Date: 06.01.2025 **End Date:** 11.01.2025

Day 1 (06.01.2025)	Day 2 (07.01.2025)	Day 3 (08.01.2025)	Day 4 (09.01.2025)	Day 5 (10.01.2025)	Day 6 (11.01.2025)
6:00 pm – 6:30 pm **Inauguration**	**6:00 pm – 7:30 pm** **Session 3** 1. **Name of the Expert:** *Dr. S. Priya* 2. **Designation:** *Vice President – Pharma, Biotech and Medtech* 3. **Organization:** *Tamilnadu Industrial Development Corporation Limited* 4. **Experience in Years:** *15 Years* 5. **Topic to be taught:** *Virtual and Augmented Reality*	**6:00 pm – 7:30 pm** **Session 5** 1. **Name of the Expert:** *Dr. V. Sathiesh Kumar* 2. **Designation:** *Assistant Professor (Senior Grade)* 3. **Organization:** *MIT Campus, Anna University* 4. **Experience in Years:** *12 Years* 5. **Topic to be taught:** *Vision Transformer and its Applications* 1.	**6:00 pm – 7:30 pm** **Session 7** 1. **Name of the Expert:** *Dr. John Jose* 2. **Designation:** *Associate Professor, Dept. of Computer Science and Engineering* 3. **Organization:** *IIT Guwahati* 4. **Experience in Years:** *21 Years* 5. **Topic to be taught:** *Multicore Architectures and Systems*	**6:00 pm – 7:30 pm** **Session 9** 1. **Name of the Expert:** *Mr. Deepan Raj* 2. **Designation:** *Senior Technical Lead* 3. **Organization:** *HCL Technologies* 4. **Experience in Years:** *7 Years* 5. **Topic to be taught:** *Architectural advancements in Anomaly detection*	**2:00 pm – 3:30 pm** **Session 11** 1. **Name of the Expert:** *Mr. Deepan Raj* 2. **Designation:** *Senior Technical Lead* 3. **Organization:** *HCL Technologies* 4. **Experience in Years:** *7 Years* 5. **Topic to be taught:** *Evolution of GPT models*
6:30 pm – 8:00 pm **Session 1** 1. **Name of the Expert:** *Dr. I. A. Palani* 2. **Designation:** *Professor* 3. **Organization:** *Indian Institute of Technology Indore* 4. **Experience in Years:** *15 Years* 5. **Topic to be taught:** *Industry 4.0 & Beyond*	**7:30 pm – 9:00 pm** **Session 4** 1. **Name of the Expert:** *Dr. Jinshan Tang* 2. **Designation:** *Professor* 3. **Organization:** *College of Computing, Michigan Technological University, Michigan, United States* 4. **Experience in Years:** *18 Years* 5. **Topic to be taught:** *Automatic Detection and Segmentation of COVID-19 Infections from Medical Images with Deep CNNs*	**7:30 pm – 9:00 pm** **Session 6** 1. **Name of the Expert:** *Dr. V. Masillamani* 2. **Designation:** *Professor, Dept. of Computer Science and Engineering* 3. **Organization:** *IIITDM Kancheepuram* 4. **Experience in Years:** *19 Years* 5. **Topic to be taught:** *Predictive Analytics*	**7:30 pm – 9:00 pm** **Session 8** 1. **Name of the Expert:** *Dr. Mariofanna (Fanny) Milanova* 2. **Designation:** *Professor of Computer Science Department* 3. **Organization:** *University of Arkansas Little Rock, AR 72204, USA* 4. **Experience in Years:** *20 Years* 5. **Topic to be taught:** *The Art of Possibility: Blending Human Augmentation and Generative AI for Creative Mastery*	**7:30 pm – 9:00 pm** **Session 10** 1. **Name of the Expert:** *Dr.C. Mohanraj* 2. **Designation:** *Senior Manager – AI ML Technical Lead* 3. **Organization:** *Standard Charted* 4. **Experience in Years:** *12 Years* 5. **Topic to be taught:** *AI in Industry 4.0*	**3:30 pm – 5:00 pm** **Session 12** 1. **Name of the Expert:** *Mrs. A. Navis Nirmal* 2. **Designation:** *Senior Software Engineer* 3. **Organization:** *Aptiv TCI, Bangalore* 4. **Experience in Years:** *5 Years* 5. **Topic to be taught:** *Testing for ADAS & automotive embedded firmware development*
8:00 pm – 9:30 pm **Session 2** 1. **1. Name of the Expert:** *Dr. S. Balakrishnan* 2. **Designation:** *Associate Professor* 3. **Organization:** *Vellore Institute of Technology, Vellore* 4. **Experience in Years:** *11 Years* 5. **Topic to be taught:** *An introduction to Quantum Algorithms*					**5:00 pm – 6:30 pm** **Session 13** 1. **Name of the Expert:** *Mr. Jai Ganesh* 2. **Designation:** *Senior AI - Architect* 3. **Organization:** *Valeo Detection Systems* 4. **Experience in Years:** *10 Years* 5. **Topic to be taught:** *Deep Insights of VAE & GAN Architecture with Deep Fake Hands-on*
					6:30 pm to 7:30 pm **Online Test & Feedback**
					7:30 pm to 8:00 pm **Valedictory Session**