

**Faculty of Information and Communication Engineering**

**M.E. Electronics and Communication Engineering**

**(R 2017) Semester – I**

**EL5111 Communication and Signal Processing Laboratory**

**(Requirements for a batch of 25 students)**

<b>Sl. No.</b>	<b>Description of Equipment</b>	<b>Quantity required (R)</b>	<b>Quantity available (A)</b>	<b>Deficiency (R - A)</b>
1.	PC	12		
2.	SDR Board	12		

**Faculty of Information and Communication Engineering**

**M.E. Electronics and Communication Engineering**

**(R 2017) Semester – II**

**EL5211 Advanced Electronics System Laboratory**

**(Requirements for a batch of 25 students)**

<b>Sl. No.</b>	<b>Description of Equipment</b>	<b>Quantity required (R)</b>	<b>Quantity available (A)</b>	<b>Deficiency (R - A)</b>
1.	32 Bit ARM 7 Micro Controller	4		
2.	FPGA Board	4		
3.	Desktop	4		
4.	Verilog / VHDL Software	8		